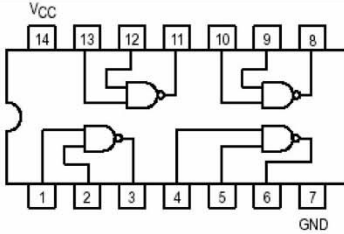
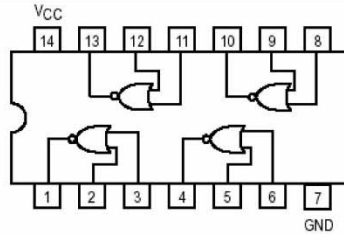
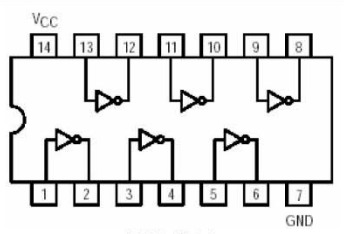
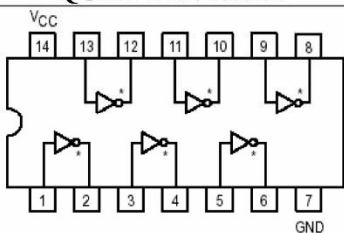
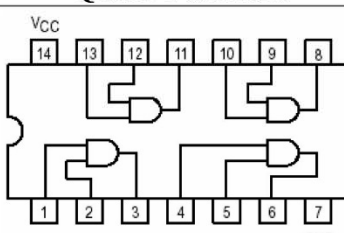
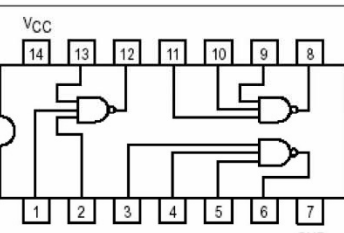
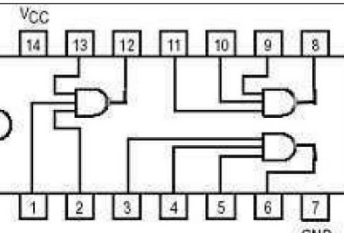
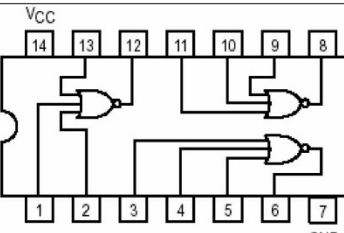
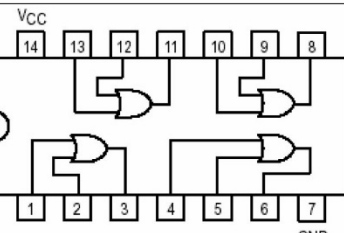
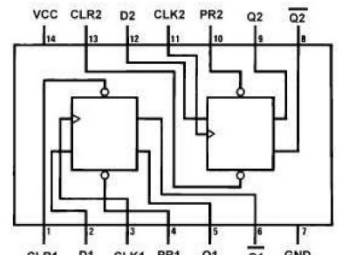
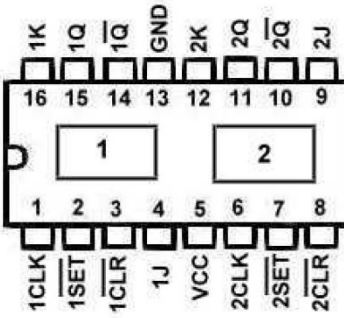
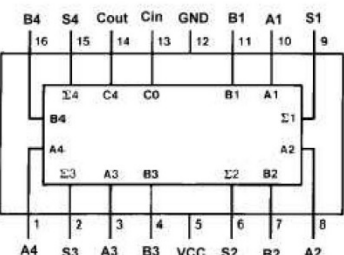
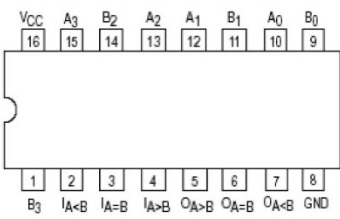
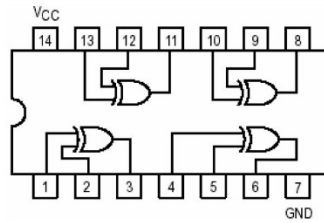


# INTEGRATED CIRCUIT PIN LAYOUT

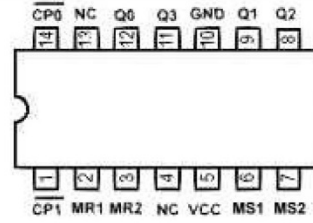
 <p><b>74LS00</b> <b>QUAD 2-IN NAND</b></p>	 <p><b>74LS02</b> <b>QUAD 2-IN NOR</b></p>	 <p><b>74LS04</b> <b>HEX INVERTER</b></p>
 <p>* OPEN COLLECTOR OUTPUTS <b>74LS05</b> <b>HEX INVERTER</b></p>	 <p><b>74LS08</b> <b>QUAD 2-IN AND</b></p>	 <p><b>74LS10 &amp; 7410</b> <b>TRIPPLE 3-IN NAND</b></p>
 <p><b>74LS11</b> <b>TRIPLE 3-IN AND</b></p>	 <p><b>74LS27</b> <b>TRIPLE 3-IN NOR</b></p>	 <p><b>74LS32</b> <b>QUAD 2-IN OR</b></p>
 <p><b>74LS74</b> <b>DUAL D-FLIP FLOP</b></p>	 <p><b>74LS76</b> <b>DUAL J-K FLIP FLOP</b></p>	 <p><b>74LS83</b> <b>4-BIT FULL ADDER</b></p>



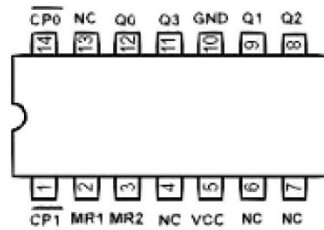
**74LS85**  
**4-BIT MAG. COMPARATOR**



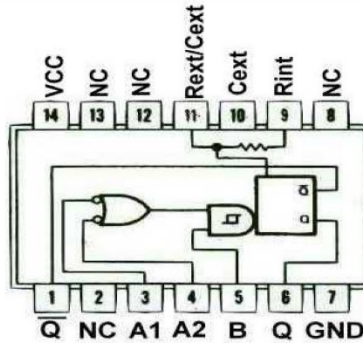
**74LS86**  
**QUAD 2-IN EXOR**



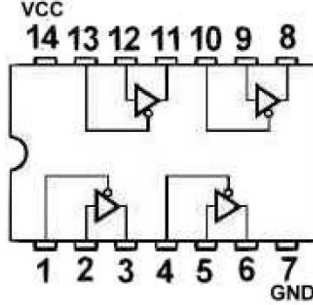
**74LS90**  
**DECADE COUNTER**



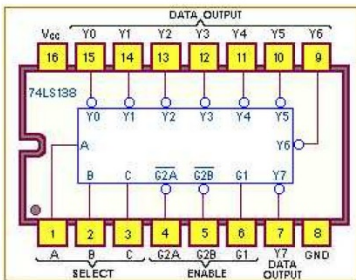
**74LS93**  
**4-BIT COUNTER**



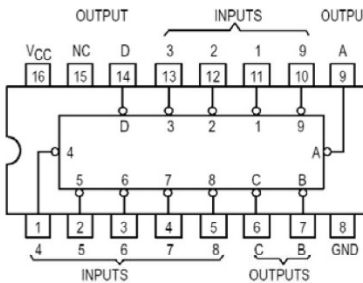
**74LS121**  
**MONOSTABLE MULTI**



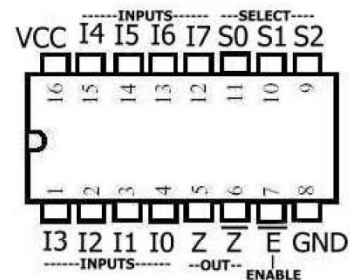
**74LS125**  
**QUAD TRISTATE BUFFER**



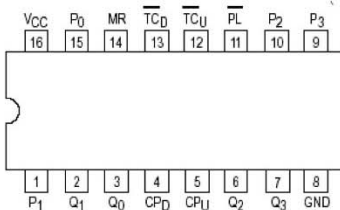
**74LS138**  
**3-to-8 DECODER/DEMUX**



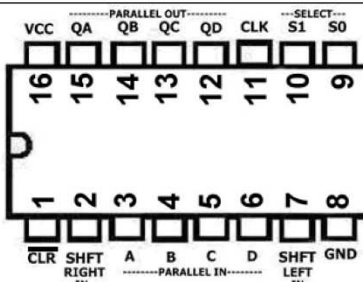
**74LS147**  
**10-TO-4 BCD PRIORITY ENCODER**



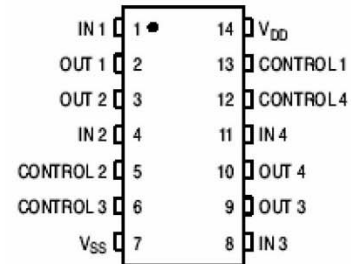
**74LS151**  
**1-of-8 SELECTOR/MUX**



**74LS193**  
**PRESETTABLE UP/DOWN COUNTER**



**74LS194**  
**4-BIT SHIFT REGISTER**



**CD4016**  
**QUAD BILATERAL SWITCH**